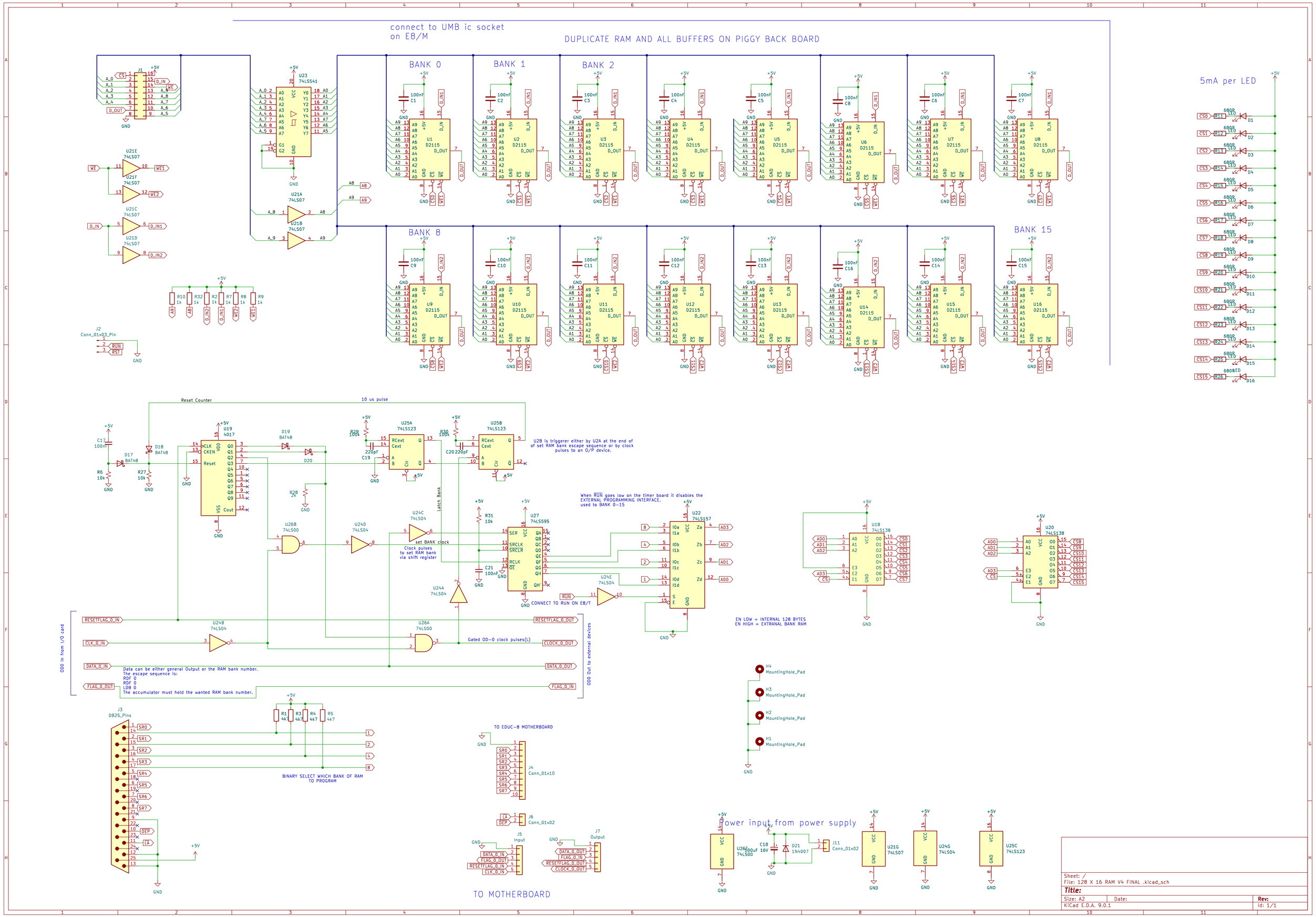
connect to UMB ic socket
on E8/M
DUPLICATE RAM AND ALL BUFFERS ON PIGGY BACK BOARD
BANK 0
BANK 1
BANK 2
BANK 8
BANK 15
5mA per LED
Reset Counter
10 us pulse
U25A 74LS123
U25B 74LS123
U2B is triggerer either by U2A at the end of of set RAM bank escape sequence or by clock pulses to an O/P device.
U19 4017
U23 74LS541
U22 74LS157
U27 74LS595
U18 74LS138
U20 74LS138
When RUN goes low on the timer board it disables the EXTERNAL PROGRAMMING INTERFACE. used to BANK 0-15
CONNECT TO RUN ON E8/T
set BANK clock
Clock pulses to set RAM bank via shift register
Latch Bank
Gated OD-0 clock pulses(L)
ODD In from I/O card
ODD Out to external devices
Data can be either general Output or the RAM bank number.
The escape sequence is:
RDF 0
RDF 0
LDB 0
The accumulator must hold the wanted RAM bank number.
EN LOW = INTERNAL 128 BYTES
EN HIGH = EXTRANAL BANK RAM
BINARY SELECT WHICH BANK OF RAM TO PROGRAM
TO EDUC-8 MOTHERBOARD
TO MOTHERBOARD
Power input from power supply
Sheet: /
File: 128 X 16 RAM V4 FINAL .kicad_sch
Title:
Size: A2
Date:
Rev:
KiCad E.D.A. 9.0.1
Id: 1/1